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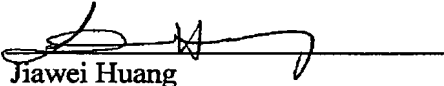
Atty Docket No. :	JCLA6643
Application No. :	09/801,350
Filing Date :	March 07, 2001
Pages :	Cover + 4

BY FACSIMILE ONLY

Fax No. :	571-273-8300
Attention :	EXAMINER : NADAV, ORI
Group Unit :	2811
From :	Jiawei Huang, Reg. No. 43,330
MESSAGE :	Enclosed herewith is a Response in 2 pages and a copy of Notification of Non-Complaint Appeal Brief in 2 pages.

Sir:

I hereby certify that this correspondence is being facsimile transmitted to the United States Patent and Trademark Office on November 21, 2007 at the above indicated fax number.

Sign by: 

Jiawei Huang

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Examiner: NADAV, ORI

Group Art Unit: 2811

In re PATENT APPLICATION of

Applicants : Lai et al.)

Serial No. : 09/801,350)

Filed : March 07, 2001)

For : Electrostatic Discharge Protection)

Circuit Coupled On I/O Pad)

) Attorney Docket: JCLA6643

) _____

RESPONSE TO NOTIFICATION OF NON-COMPLAINT APPEAL BRIEF

Mail Stop Appeal Brief- Patents

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

In response to the NOTIFICATION OF NON-COMPLAINT APPEAL BRIEF dated October 26, 2007, of which a copy is attached hereto, Applicants respectfully submit the following corrected "SUMMARY OF THE CLAIMED SUBJECT MATTER" in the separate accompanying pages.

Respectfully submitted,
J.C. PATENTS

Date: 11-21-2007

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V. SUMMARY OF CLAIMED SUBJECT MATTER

The claimed subject matter of independent claim 1 involved in the appeal is directed to an electrostatic discharge (ESD) protection circuit, suitable for use on the I/O pad, the ESD protection circuit, capable of to prevent activation of the SCR circuit during the normal IC operation (as shown in FIG. 4 and as discussed in Line 9 of Page 10 to Line 8 of Page 11, and line 17 of Page 12 to Line 14 of Page 13). The electrostatic discharge (ESD) protection circuit comprises a silicon controlled rectifier (SCR) circuit 104 and an anti-latch-up circuit 110 (as shown FIG. 4). The SCR circuit 104 comprises a first connection terminal 112, a second connection terminal 114, and a third connection terminal 116, wherein the first connection terminal 112 and the second connection terminal 114 are respectively connected to the I/O pad 100 and a ground voltage GND, (as shown in Figure 4, and as discussed in Line 14 of Page 10 to Line 2 of Page 11 of the specification). The anti-latch-up circuit 110 comprises a fourth connection terminal 126, a fifth connection terminal 128, and a sixth connection terminal 130, respectively coupled to a voltage source Vcc, the ground voltage GND, and the third connection terminal 16 of the SCR circuit 104, wherein the sixth connection terminal 116 of the anti-latch-up circuit 110 is directly connected to the third connection terminal 116 of the SCR circuit 104 (as shown in FIG. 4, Lines 3-6 of Page 11). A voltage rising rate at a node A of the anti-latch-up circuit 110 determines whether or not to trigger the SCR circuit 104 and thereby prevent latching up of the SCR circuit during normal operation (FIG. 5A-5B, and as discussed in Lines 6-8 of Page 11, and line 17 of Page 12 to Line 14 of Page 13).

The claimed subject matter of the dependent claim 15 involved in the appeal is directed to a RC delay time of the anti-latch-up circuit 110 is smaller than a voltage rising time of an IC power but greater than a voltage rising time of an ESD pulse (as discussed in Line 17 of page 12 to line 1 of page 13).



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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/801,350	03/07/2001	Chun Hsiang Lai	JCLA6643	4896

7590

10/26/2007

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EXAMINER

ART UNIT

PAPER NUMBER

DATE MAILED: 10/26/2007

Please find below and/or attached an Office communication concerning this application or proceeding.

**Notification of Non-Compliance Appeal Brief
(37 CFR 41.37)**

Application No.

09/801,350

Examiner

Ori Nadav

Applicant(s)

LAI ET AL.

Art Unit

2811

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--The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

The Appeal Brief filed on 06 September 2007 is defective for failure to comply with one or more provisions of 37 CFR 41.37.

To avoid dismissal of the appeal, applicant must file an amended brief or other appropriate correction (see MPEP 1205.03) within **ONE MONTH or THIRTY DAYS** from the mailing date of this Notification, whichever is longer. **EXTENSIONS OF THIS TIME PERIOD MAY BE GRANTED UNDER 37 CFR 1.136.**

1. ☐ The brief does not contain the items required under 37 CFR 41.37(c), or the items are not under the proper heading or in the proper order.
2. ☐ The brief does not contain a statement of the status of all claims, (e.g., rejected, allowed, withdrawn, objected to, canceled), or does not identify the appealed claims (37 CFR 41.37(c)(1)(iii)).
3. ☐ At least one amendment has been filed subsequent to the final rejection, and the brief does not contain a statement of the status of each such amendment (37 CFR 41.37(c)(1)(iv)).
4. ☒ (a) The brief does not contain a concise explanation of the subject matter defined in each of the independent claims involved in the appeal, referring to the specification by page and line number and to the drawings, if any, by reference characters; and/or (b) the brief fails to: (1) identify, for each independent claim involved in the appeal and for each dependent claim argued separately, every means plus function and step plus function under 35 U.S.C. 112, sixth paragraph, and/or (2) set forth the structure, material, or acts described in the specification as corresponding to each claimed function with reference to the specification by page and line number, and to the drawings, if any, by reference characters (37 CFR 41.37(c)(1)(v)).
5. ☐ The brief does not contain a concise statement of each ground of rejection presented for review (37 CFR 41.37(c)(1)(vi)).
6. ☐ The brief does not present an argument under a separate heading for each ground of rejection on appeal (37 CFR 41.37(c)(1)(vii)).
7. ☐ The brief does not contain a correct copy of the appealed claims as an appendix thereto (37 CFR 41.37(c)(1)(viii)).
8. ☐ The brief does not contain copies of the evidence submitted under 37 CFR 1.130, 1.131, or 1.132 or of any other evidence entered by the examiner and relied upon by appellant in the appeal, along with a statement setting forth where in the record that evidence was entered by the examiner, as an appendix thereto (37 CFR 41.37(c)(1)(ix)).
9. ☐ The brief does not contain copies of the decisions rendered by a court or the Board in the proceeding identified in the Related Appeals and Interferences section of the brief as an appendix thereto (37 CFR 41.37(c)(1)(x)).
10. ☒ Other (including any explanation in support of the above items):

It seems that the submitted "summary of claimed subject matter" is "summary of the invention" and not "summary of the claimed subject matter". Independent claim 1 is not mapped. That is, there is no concise recitation of claim 1 together with the reference to the specification.



ORI NADAV
PRIMARY EXAMINER